# A Short Introduction to Index Coding with Side Information 

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## Basic Definitions


$S$ broadcasts (2 transmissions):

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\left\{\begin{array}{l}
x_{1}+x_{2} \\
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Trivial solution: 3 transmissions

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\mathbf{M}=\left(\begin{array}{lll}
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has rank two. An $n \times n$ matrix M fits a digraph $\mathcal{D}$ of order $n$ if

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El Rouayheb et al. (2008): block nonlinear ICs outperform block linear ICs; block linear ICs outperform scalar linear ICs

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NC instance $\hookrightarrow$ IC instance
$\exists$ a linear block NC $\Longleftrightarrow \exists$ a linear block IC $\exists$ a nonlinear block NC $\Longrightarrow \exists$ a nonlinear block IC

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Matroid can be reduced to IC matroid $\hookrightarrow$ IC instance
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matroid: 2-rep. but not 1-rep.
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linear 2-block IC with better rate than linear scalar IC

Capacity: Hardness and Bounds

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( $) ~ \alpha(\mathcal{D}) \leq \frac{1}{C_{s /}(\mathcal{D})} \leq n-\nu(\mathcal{D}): \nu(\mathcal{D})$ - maximum number of disjoint circuits


## Capacity: Hardness and Bounds

## Hardness

(1) $C_{s l}$ : hard to compute


- (Dau, Skachek, Chee, 2011): $C_{s l}(\mathcal{D})=1 / 2$ ? $\left(\right.$ or $^{\operatorname{minrk}}{ }_{q}(\mathcal{D})=2$ ?) is NP-complete
- (Langberg, Sprintson, 2008): finding an IC with rate $>\alpha C_{s l}(\mathcal{D})(\alpha \in(0,1])$ is NP-hard
(2) $C_{s}$ and $C$ : the hardness is not known
- (Blasiak et al. 2011): polynomial time algo. to approx. $C$ with ratio $O\left(\frac{\log n}{n \log \log n}\right)$


## Bounds

(1) $\alpha(\mathcal{G}) \leq \frac{1}{C_{s( }(\mathcal{G})} \leq \chi(\overline{\mathcal{G}}): \chi$ - chromatic number, $\alpha(\mathcal{G})$ - independent number
(3) $\alpha(\mathcal{G}) \leq \frac{1}{C_{s l}(\mathcal{G})} \leq n-\mu(\mathcal{G}): \mu(\mathcal{G})$ - maximum size of a matching
(1) $\alpha(\mathcal{D}) \leq \frac{1}{c_{s /(\mathcal{D})}} \leq \operatorname{cc}(\mathcal{D}): \alpha(\mathcal{D})$ - size of maximum acyclic induced subgraph, $\mathrm{cc}(\mathcal{D})$ - clique cover number
(-) $\alpha(\mathcal{D}) \leq \frac{1}{C_{s /( }(\mathcal{D})} \leq n-\nu(\mathcal{D}): \nu(\mathcal{D})$ - maximum number of disjoint circuits
(3) $\alpha(\mathcal{G}) \leq b_{2} \leq \frac{1}{c(\mathcal{G})} \leq b_{n}=\chi_{f}(\overline{\mathcal{G}}): b_{2}, b_{n}$ - solutions of linear programs

Scalar Linear Capacity $C_{s I}$

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## Scalar Capacity $C_{s}$ and General Capacity $C$

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- (Bar-Yossef et al. 2006): $C_{s}=C_{s l}$ for perfect graphs, odd cycles and complements, acyclic digraphs
- (Blasiak et al. 2011): $n$-cycles $(C=2 / n)$, complements of $n$-cycles $\left(C=\frac{\lfloor n / 2\rfloor}{n}\right)$, 3-regular Cayley graphs of $\mathbb{Z}_{n}(C=2 / n)$

Error Correction for Index Coding

Error Correction for Index Coding

$$
\text { Classical ECC: } \mathbf{x} \xrightarrow{\text { encoding }} \mathbf{y}=\mathbf{x L} \xrightarrow[\text { channel }]{\text { noisy }} \mathbf{y}+\boldsymbol{\epsilon} \xrightarrow{\text { decoding }} \mathbf{x}
$$



(Dau, Skachek, Chee, 2011): scalar linear error-correcting index code (ECIC)

- optimal IC + optimal ECC $\neq$ optimal ECIC for small alphabets

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Security for Index Coding

A scalar linear IC: $\mathbf{x} \mapsto \mathbf{x L}$, where $\mathbf{L}$ is an $n \times k$ matrix; Rate: $1 / k$

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(Dau, Skachek, Chee, 2011):

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[^0]
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